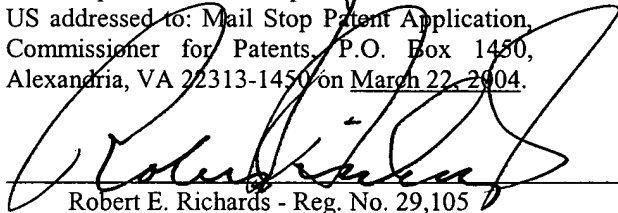


IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:)
)
Makoto Yamamoto et al.)
) Art Unit: **2814**
 Serial No. To Be Assigned)
) Examiner: **Shrinivas H. Rao**
 Filed: **Concurrently Herewith**)
 (Divisional Application of 10/014,949 filed October 26, 2001))
)
 For: **Method of Fabricating Semiconductor Integrated**)
Circuit)

I hereby certify that this correspondence is being deposited with the United States Postal Service in an Express Mail envelope NO. EL 999329032 US addressed to: Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 22, 2004.


 Robert E. Richards - Reg. No. 29,105

PRELIMINARY AMENDMENT

Mail Stop Patent Application
 Commissioner for Patents
 P.O. Box 1450
 Alexandria, VA 22313-1450

Sir:

Please enter the following amendments in the patent application identified above before examining the application.

Amendments to the Specification begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.